

THE INVENTION CLAIMED IS

1. A program writable IC card, comprising:  
a microprocessor;

a memory for storing a program write control  
5 program having at least one of a decryption function  
and a function for converting a program code and a  
program for said microprocessor; and

means in said microprocessor for performing at  
least one of said decryption process and said process  
10 of converting a program code according to said program  
write control program and storing said program in said  
memory when said program for said microprocessor is  
given from the outside.

2. A program writable IC card, comprising:  
15 a microprocessor;

a memory for storing a program write control  
program having a decryption function and a program for  
said microprocessor; and

means in said microprocessor for performing said  
20 decryption process according to said program write  
control program and storing said program in said  
memory when said crypted program for said  
microprocessor is given from the outside.

3. A program writable IC card, comprising:  
25 a microprocessor;

a memory for storing a program write control program having a function for converting a program code and a program for said microprocessor; and

means in said microprocessor for performing said  
5 process for converting a program code according to said program write control program and storing said program in said memory when said program for said microprocessor is given from the outside.

4. A program writable IC card according to Claim  
10 2, wherein said program write control program, further comprising:

a function for converting a program code; and  
said microprocessor further comprising:

means for performing said decryption process and  
15 said process of converting a program code according to said program write control program and storing said program in said memory when said crypted program for said microprocessor is given from the outside.

5. A program writable IC card, comprising:  
20 a first and a second microprocessor;  
an external signal input unit;  
switching means for switching a signal inputted from said external signal input unit to said first microprocessor or said second microprocessor; and  
25 a memory for storing a program write control

program having at least one of a decryption function and a function for converting a program code and a program for said first microprocessor inputted from said external signal input unit; and

5       said second microprocessor, comprising;

          means in said second microprocessor for performing at least one of said decryption process and said process of converting a program code according to said program write control program and storing said program in said memory when said program for said first  
10       microprocessor is given from the outside.

          6. A program writable IC card, comprising:  
          a first and a second microprocessor;  
          an external signal input unit;

15       switching means for switching a signal inputted from said external signal input unit to said first microprocessor or said second microprocessor; and

          a memory for storing a program write control program having a decryption function and a program for  
20       said first microprocessor inputted from said external signal input unit; and

          said second microprocessor, comprising;

          means in said second microprocessor for performing said decryption process according to said program  
25       write control program and storing said program in said

memory when said crypted program for said first microprocessor is given from the outside.

7. A program writable IC card, comprising:  
a first and a second microprocessor;

5 an external signal input unit;

switching means for switching a signal inputted from said external signal input unit to said first microprocessor or said second microprocessor; and

10 a memory for storing a program write control program having a function for converting a program code and a program for said first microprocessor inputted from said external signal input unit; and

said second microprocessor, comprising;

15 means in said second microprocessor for performing said process for converting a program code according to said program write control program and storing said program in said memory when said program for said first microprocessor is given from the outside.

20 8. A program writable IC card according to Claim 6, wherein said program write control program, further comprising:

a function for converting a program code; and  
said second microprocessor further comprising:

25 means for performing said decryption process and said process of converting a program code according to

said program write control program and storing said program in said memory when said crypted program for said first microprocessor is given from the outside.

9. A program writable IC card, comprising:

5 a microprocessor whose information is not opened to public and a microprocessor whose information is opened to public;

an external signal input unit;

10 a memory for storing a program for said microprocessor whose information is not opened to public and a program for said microprocessor whose information is opened to public;

15 means for accessing said memory for storing a program only from said microprocessor whose information is not opened to public;

switching means for switching a signal inputted from said external signal input unit to said microprocessor whose information is not opened to public or said microprocessor whose information is opened to public; and

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a memory for storing said program by said microprocessor whose information is not opened to public when said program for said microprocessor whose information is opened to public is given from the outside.

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10. A program writable IC card, comprising:

a microprocessor whose information is not opened to public and a microprocessor whose information is opened to public;

5 an external signal input unit;

a memory for storing a program for said microprocessor whose information is not opened to public and a program for said microprocessor whose information is opened to public;

10 means for accessing said memory for storing a program only from said microprocessor whose information is not opened to public;

switching means for switching a signal inputted from said external signal input unit to said

15 microprocessor whose information is not opened to public or said microprocessor whose information is opened to public; and

a memory for storing said program by said microprocessor whose information is opened to public when said program for said microprocessor whose information is opened to public is given from the outside.

11. A program writable IC card according to one of Claims 1 to 10, wherein said memory for storing a program for said microprocessor is a memory which can

write only once.

12. A processing method for a program writable IC card having a microprocessor and a memory for storing a program for said microprocessor, comprising:

5       a step of performing at least one of a decryption process and a process of converting a program code for said program according to a program write control program when said program for said microprocessor is given from the outside; and

10       a step of storing a processing result of at least one of said decryption process and said process of converting a program code in said memory.

13. A processing method for a program writable IC card having a microprocessor and a memory for storing a program for said microprocessor, comprising:

15       a step of performing a decryption process for said program according to a program write control program when said crypted program for said microprocessor is given from the outside; and

20       a step of storing a processing result of said decryption process in said memory.

14. A processing method for a program writable IC card having a microprocessor and a memory for storing a program for said microprocessor, comprising:

25       a step of performing a process of converting a

program code for said program according to a program write control program when said program for said microprocessor is given from the outside; and

5 a step of storing a processing result of said process of converting a program code in said memory.

15. A processing method for a program writable IC card having a first and a second microprocessor and a memory for storing a program for said first microprocessor, comprising:

10 a step of performing at least one of a decryption process and a process of converting a program code, which is a process by said second microprocessor, for said program according to a program write control program when said program for said first

15 microprocessor is given from the outside; and

a step of storing a processing result of at least one of said decryption process and said process of converting a program code in said memory by said second microprocessor.

20 16. A processing method for a program writable IC card having a first and a second microprocessor and a memory for storing a program for said first microprocessor, comprising:

25 a step of performing a decryption process, which is a process by said second microprocessor, for said



program according to a program write control program  
when said crypted program for said first  
microprocessor is given from the outside; and

5 a step of storing a processing result of said  
decryption process in said memory by said second  
microprocessor.

10 17. A processing method for a program writable IC  
card having a first and a second microprocessor and a  
memory for storing a program for said first  
microprocessor, comprising:

15 a step of performing a process for converting a  
program code, which is a process by said second  
microprocessor, for said program according to a  
program write control program when said program for  
said first microprocessor is given from the outside;  
and

a step of storing a processing result of said  
process for converting a program code in said memory  
by said second microprocessor.

20 18. A processing method for a program writable IC  
card having a first and a second microprocessor and a  
memory for storing a program for said first  
microprocessor, comprising:

25 a step of storing a program for said first  
microprocessor whose information is not opened to

public and a program for a microprocessor whose information is opened to public in a memory;

a step of accessing said memory for storing a program only from said microprocessor whose  
5 information is not opened to public;

a step of switching a signal inputted from said external signal input unit to said microprocessor whose information is not opened to public or said microprocessor whose information is opened to public;  
10 and

a step of storing said program by said microprocessor whose information is not opened to public when said program for said microprocessor whose information is opened to public is given from the  
15 outside.

19. A processing method for a program writable IC card having a first and a second microprocessor and a memory for storing a program for said first microprocessor, comprising:

20 a step of storing a program for said first microprocessor whose information is not opened to public and a program for a microprocessor whose information is opened to public in a memory;

a step of accessing said memory for storing a  
25 program only from said microprocessor whose

information is not opened to public;

5 a step of switching a signal inputted from said  
external signal input unit to said microprocessor  
whose information is not opened to public or said  
microprocessor whose information is opened to public;  
and

10 a step of storing said program by said  
microprocessor whose information is opened to public  
when said program for said microprocessor whose  
information is opened to public is given from the  
outside.